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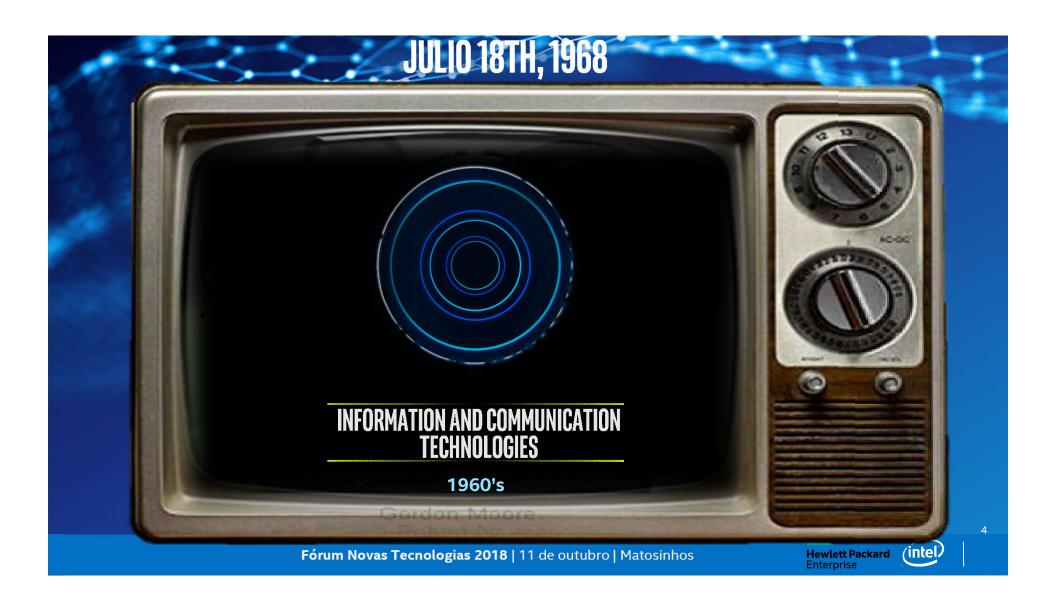
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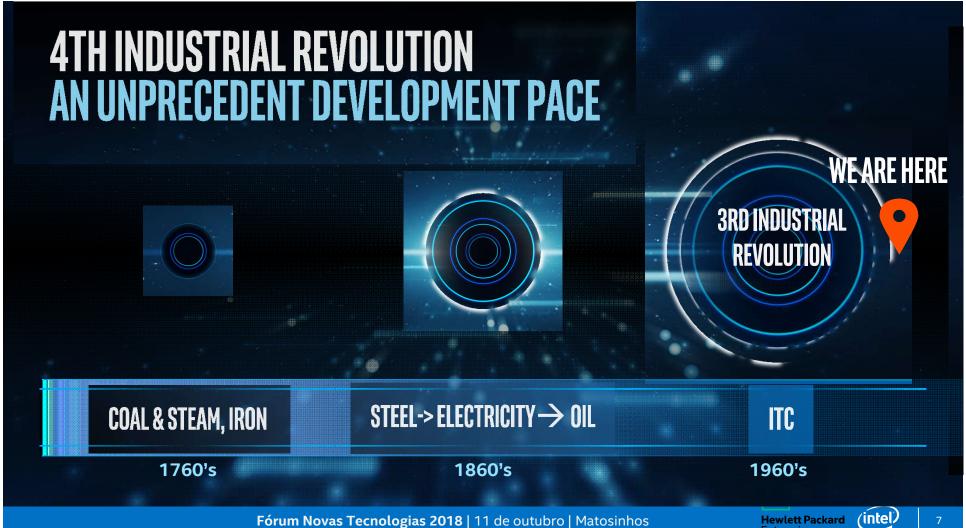




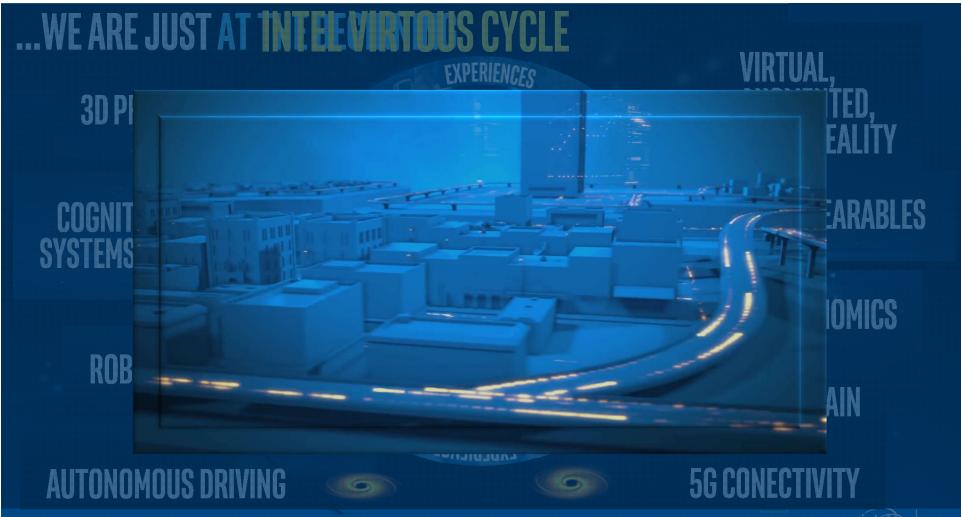


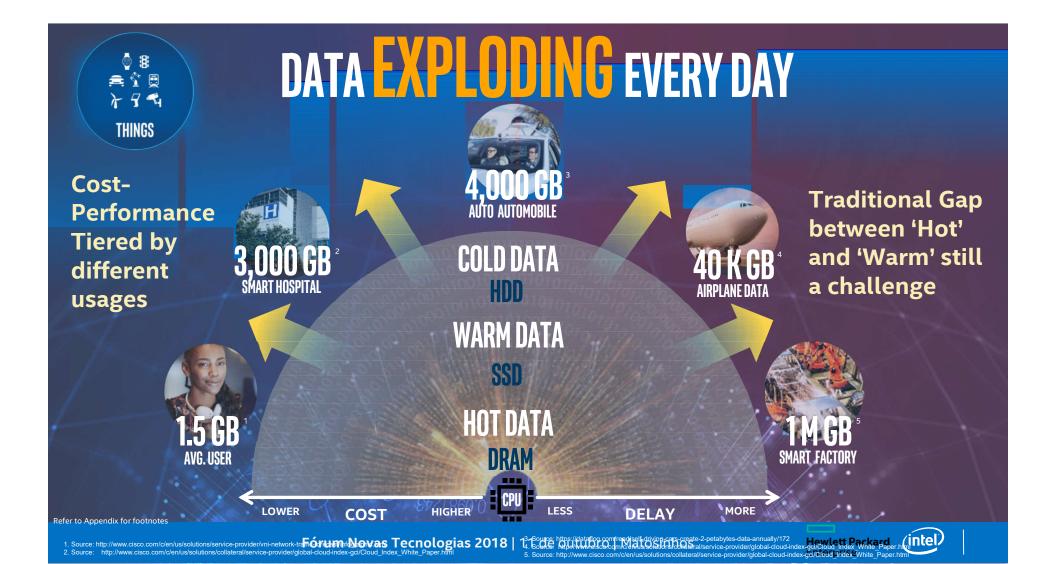
ose Half Of Its Market **EXCLUSIVE** on In 2 Years verge of a steady decline **NVIDIA CEO Declares** l bloggers just GPUs will soon replace CP tence canning game over for Intel





Enterprise





INTEL NOW A DATA-CENTRIC COMPANY

DATA-CENTRIC INFRASTRUCTURE

MOVE FASTER









STOREMORE





PROCESS EVERYTHING









NEXT INTEL® XEON® SCALABLE PROCESSOR

CASCADE LAKE

WITH INTEL® OPTANE™ DC PERSISTENT MEMORY

Leadership performance

Optimized Cache Hierarchy

Higher Frequencies

Support For (intel) OPTANE DC (3) PERSISTENT MEMORY

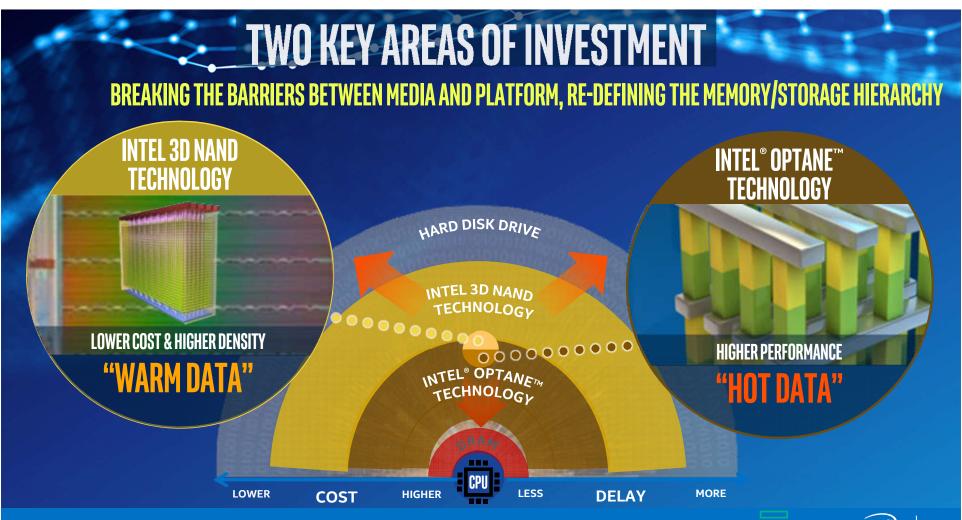
Security Mitigations

Optimized Frameworks & Libraries

Fórum Novas Tecnologias 2018 | 11 de outubro | Matosinhos

Hewlett Packard Enterprise





3 REASONS WHY THE INTEL® OPTANE™ SSD IS THE IDEAL CACHING SOLUTION



LOWER AND MORE CONSISTENT LATENCY

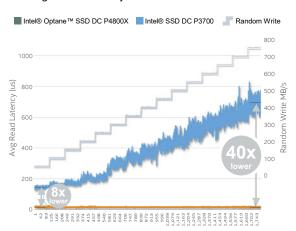


HIGHER ENDURANCE



MORE EFFICIENT

Average Read Latency under Random Write Workload¹

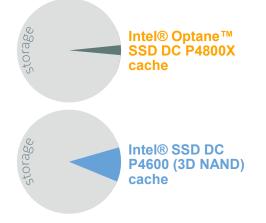


Terabytes Written Specifications (TBW)²

Intel® Optane™ SSD DC P4800X 608

Intel® SSD DC P4600 (3D NAND)

Cache as a % of Storage Capacity³



LOWER LATENCY + HIGHER ENDURANCE = GREATER SDS SYSTEM EFFICIENCY

- 1. Responsiveness defined as average read latency measured at queue depth 1 during 4k random write workload. Measured using FIO 2.15. Common Configuration Intel 2U Server System, OS CentOS 7.2, kernel 3.10.0-327.el7x86_64, CPU 2 x Intel® Xeon® E5-2699 v4 @ 2.20GHz (22 cores), RAM 396GB DDR @ 2133MHz. Configuration Intel® Optane™ SSD DC P4800X 375GB and Intel® SSD DC P3700 1600GB. Latency Average read latency measured at QD1 during 4K Random Write operations using fio-2.15.
- 2. Source Intel Data Sheet: Random/JEDEC up to 2.9 DWPD (5 Years) / 21.7 PBW, sequential workload up to 4 DWPD (5 Years) / 29.2 PBW
- 3. Source Intel: General proportions shown for illustrative purposes. Refer to slide 10 for a real-world example of cache to storage ratios





Trazido até si pela HPE e pela Intel®

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